



PATENT APPLICATION

42P10047C

IFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Jian Li et al.

Serial No.: 10/762,955

Filed: January 21, 2004

Group Art Unit: 2813

Examiner: L. Schillinger

For: **LOW-VOLTAGE AND INTERFACE DAMAGE-FREE POLYMER MEMORY DEVICE**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313 on:

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RESPONSE TO RESTRICTION REQUIREMENT

COMMISSIONER FOR PATENT
PO BOX 1450
ALEXANDRIA, VA 22313

SIR:

Applicant believes the following is a complete response to the Office action mailed December 17, 2004. Please consider the following comments.